

GRAU 1763 #

24 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al Art Unit : 1765
 Serial No.: 08/690,747 Examiner : Robert Kunemund
 Filed : August 1, 1996
 Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents
 Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
 The required fees are computed below. Please apply any charges
 not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	45	-	25	=	20	\$360
Independent	14	-	11	=	3	\$234

Applicant hereby petitions under 37 C.F.R. §1.136 for
 a 2 month extension of time.

\$380

TOTAL FEE DUE

\$974

A check for \$974 is attached.

Respectfully submitted,

Date: 6/17/00

Scott C. Harris
 Reg. No. 32,030

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this
 correspondence is being deposited with the
 United States Postal Service as first class mail
 with sufficient postage on the date indicated
 below and is addressed to the Commissioner for
 Patents, Washington, D.C. 20231.

Date of Deposit 6/17/00

Signature Scott C. Harris

Typed or Printed Name of Person Signing
 Certificate Scott C. Harris